



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#13/Hmt-2871
12-19-01
Dade

In re application of:

Kimikatsu SHOJI, et al.

Appl. No. 09/377,740

Filed: August 20, 1999

For: SEMICONDUCTOR
INTEGRATED CIRCUIT AND
METHOD OF MANUFACTURING
THE SAME

Art Unit: 2811

Examiner: S. CRANE

Atty. Docket No. 32014-150502

Customer No.



26694

PATENT TRADEMARK OFFICE

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DEC 17 2001
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DEC 17 2001

Amendment and Reply Under 37 C.F.R. §§ 1.111 and 1.121

Honorable Commissioner for Patents
Box Amendments
Washington, D.C. 20231

Sir:

In reply to the non-final Office Action dated September 13, 2001, Applicants submit the following Amendment and Reply.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are needed to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims), and any other fee deficiency are hereby authorized to be charged, or any overpayments credited, to our Deposit Account No. 22-0261.